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Hayes

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/512,149
Filing Date February 23, 2000
Inventor Vishnu K. Agarwal
Assignee Micron Technology, Inc.
Group Art Unit 2814
Examiner M. Pizarro Crespo
Attorney's Docket No. MI22-1322
Title: Integrated Circuitry Including a Capacitor with an Amorphous and a Crystalline High
K Capacitor Dielectric Region



INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

Assistant Commissioner for Patents

Washington, D.C. 20231

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REMARKS

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these patents is respectfully requested.

This Information Disclosure Statement is being filed within three months of the filing date of the RCE application or before the mailing of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37

C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Respectfully submitted,

Date:

Aug 24, 2001

By:



Bernard Berman
Reg. No. 37,279
WELLS, ST. JOHN, ROBERTS,
GREGORY & MATKIN P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828
(509) 624-4276

2814

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/512,149
	Filing Date	February 23, 2000
	First Named Inventor	Vishnu K. Agarwal
	Group Art Unit	2814
	Examiner Name	M P. Crespo
Total Number of Pages in This Submission		Attorney Docket Number MI22-1322

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Bernard Berman; Wells, St John
Signature	
Date	Aug 24, 2001

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